

TOSHIBA Multi-chip Device Silicon P Channel MOS Type (U-MOSIV) /Silicon NPN Epitaxial Type

TPCP8J01

Notebook PC Applications Portable Equipment Applications

- Lead(Pb)-Free
- Small mounting area due to small and thin package
- Low drain-source ON resistance: P Channel RDS (ON) = 27 m Ω (typ.)
- High forward transfer admittance: P Channel $|Y_{fs}| = 9.6 \text{ S (typ.)}$
- Low leakage current: $IDSS = -10 \mu A (max)(VDS = -32 V)$
- Enhancement-mode: P Channel $V_{th} = -0.8$ to -2.0 V

$$(V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA})$$

Absolute Maximum Ratings (Ta = 25°C)

MOSFET

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	₹32	> v
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$)	V _{DGR}	-32	V
Gate-source voltage		Vgss	±20	V
Drain current	DC (Note 1)	ID <	-5.5	A
Diam current	Pulse (Note 1)	IDP	- 22	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
Drain power dissipati	on (t = 5 s)	PD	2.14	w
	(Note 2a)		2.14	\wedge
Drain power dissipati	on $(t = 5 s)$	(PD)	1.06	w
	(Note 2b)		1.00	
Single pulse avalance	he energy	EAS	5.8	mJ
	(Note 3)		(7/4)	
Avalanche current		√ I _{AR}	_3	Α
Repetitive avalanche	energy (Note 4)	EAR	0.21	mJ

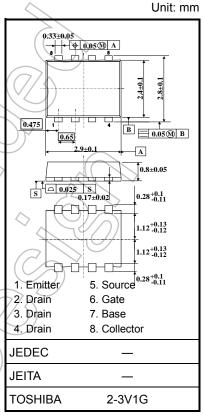
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Characteris	Symbol	Rating	Unit	
Collector-base voltage		Vсво	50	V
Collector-emitter voltage	VCEO	50	V	
Emitter-base voltage	(1)	V _{EBO}	6	V
Collector current	DC (Note 1)	Ic	100	mA
Collector power dissipation	PC	200	mW	

Note: For Notes 1 to 5, refer to the next page.

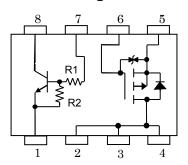
This transistor is an electrostatic-sensitive device. Handle with caution.

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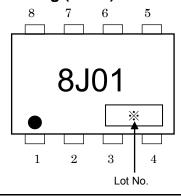


Weight: 0.011 g (typ.)

Circuit Configuration



Marking (Note5)





Common Absolute Maximum Ratings (Ta=25°C)

Characteristics	Symbol	Rating	Unit
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

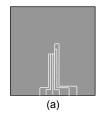
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t=5\;\text{s}) \tag{Note 2a}$	Rth (ch-a)	58.4	°C/W
Thermal resistance, channel to ambient $(t=5\ s) \eqno(Note\ 2b)$	Rth (ch-a)	117.9	°C/W

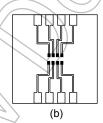
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



FR-4 25.4 × 25.4 × 0.8 (Unit: mm)



FR-4 $25.4\times25.4\times0.8$ (Unit: mm)

Note 3: VDD = -24 V, $Tch = 25^{\circ}\text{C}$ (initial), L = 0.2 mH, $RG = 25 \Omega$, IAR = -3.0 A

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: "•" on the lower left of the marking indicates Pin 1.

Weekly code (three digits):

Week of manufacture

(01 for the first week of the year, continues up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)



Electrical Characteristics (Ta = 25°C)

MOSFET

Cha	racteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curi	rent	IGSS	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curre	ent	IDSS	$V_{DS} = -32 \text{ V}, V_{GS} = 0 \text{ V}$	1	_	-10	μА
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-32	_		V
Diam-source bree	ikdowii voitage	V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	_15_	<i>!)</i> '_		V
Gate threshold vo	ltage	V_{th}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{mA}$	-0.8	_	-2.0	V
Drain aguras ON	raciatanas	Dog (ON)	V _G S = -4 V, I _D = -3.0 A	\rightarrow	38	49	mO
Drain-source ON resistance		RDS (ON)	$V_{GS} = -10 \text{ V}, I_D = -3.0 \text{ A}$	_	27	35	mΩ
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_{D} = -3.0 \text{ A}$	4.8	9.6	_	S
Input capacitance		Ciss		_	1760		
Reverse transfer	capacitance	C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	200	<i>/</i>	pF
Output capacitand	ce	Coss		-/-	210		
Switching time	Rise time	t _r	V _{GS} 0 V I _D = -3.0 A		2.8) –	
	Turn-on time	t _{on}	VDD ≈ -12 N	7/0	12		ns
	Fall time	t _f			22		115
	Turn-off time	toff	Duty ≤ 1%, t _w = 10 μs) —	90		
Total gate charge (gate-source plus		Qg	$V_{DD} \simeq -24 \text{ V}, V_{GS} = -10 \text{ V},$		34	_	_
Gate-source charge 1		Q _{gs1}	$I_D = -5.5 \text{ A}$		4.7		nC
Gate-drain ("mille	r") charge	Qgd		_	7.2	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol Test Condition	Min	Тур.	Max	Unit
Drain reverse current (Pulse) (Note 1)	IDRP —	_	_	-22	Α
Forward voltage (diode)	V _{DSF} I _{DR} = -5.5 A, V _{GS} = 0 V	_	_	1.2	V



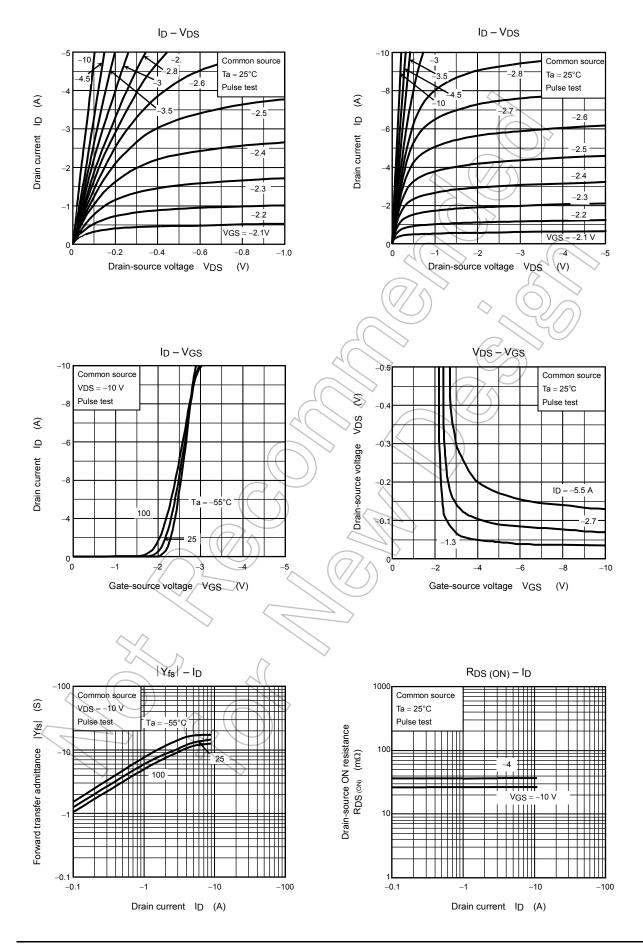
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Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
0 11 1 1 11	Ісво	V _{CB} = 50 V, I _E = 0	_	_	100	n 1
Collector cut-off current	ICEO	V _{CB} = 50 V, I _E = 0	_	_	500	nA
Emitter cut-off current	IEBO	V _{EB} = 6 V, I _C = 0	0.081	_	0.15	mA
DC current gain	hFE	V _{CE} = 5 V, I _C = 10 mA	80	_	_	
Collector-emitter saturation voltage	V _{CE} (sat)	I _C = 5 mA, I _B = 0.25 mA	1	0.1	0.3	V
Input voltage (ON)	V _I (ON)	V _{CE} = 0.2 V, I _C = 5 mA	0.7	_	1.8	V
Input voltage (OFF)	V _I (OFF)	V _{CE} = 5 V, I _C = 0.1 mA	0.5	_	1.0	V
Transition frequency	f _T	V _{CE} = 10 V, I _C = 5 mA		250	_	MHz
Collector output capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	3	6	pF
Input resistor	R1	7	7	10	13	kΩ
Resistor ratio	R1/R2		0.191	0.213	0.232	

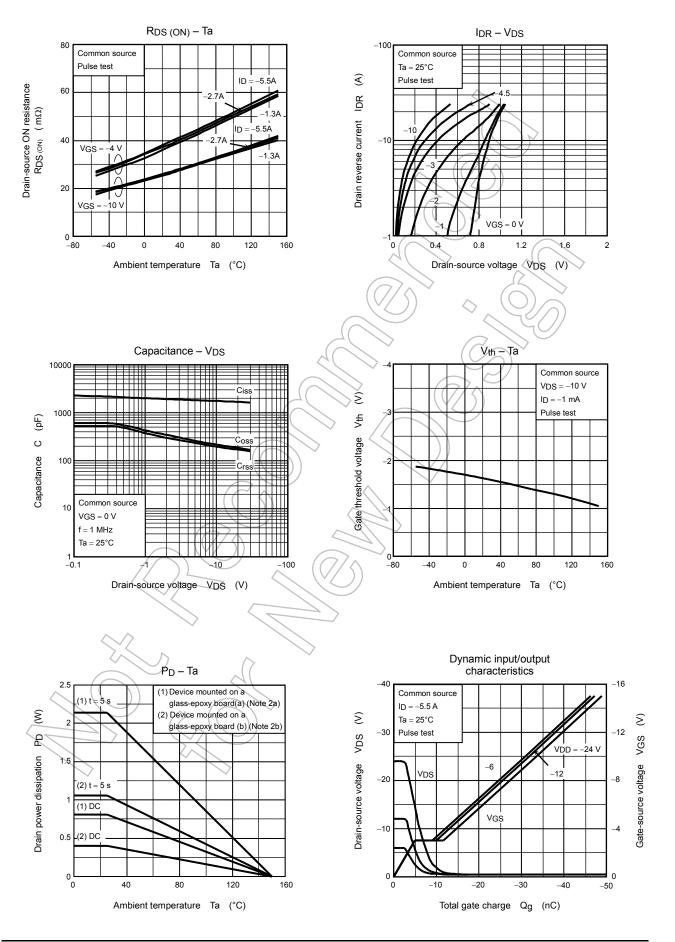




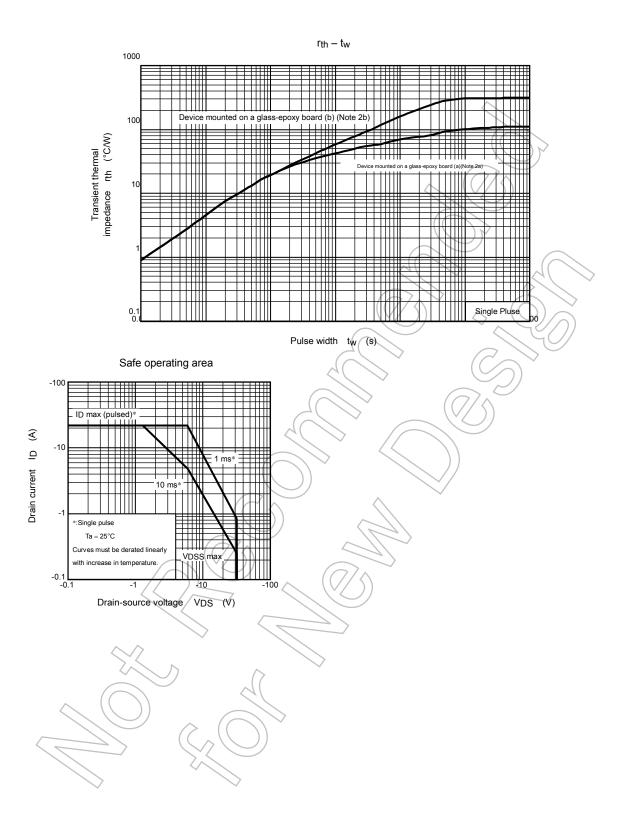
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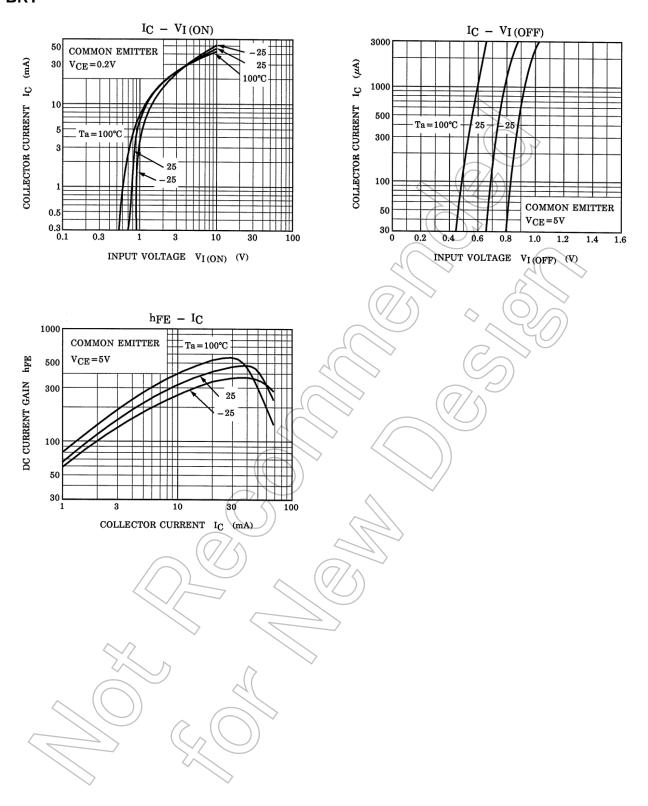








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